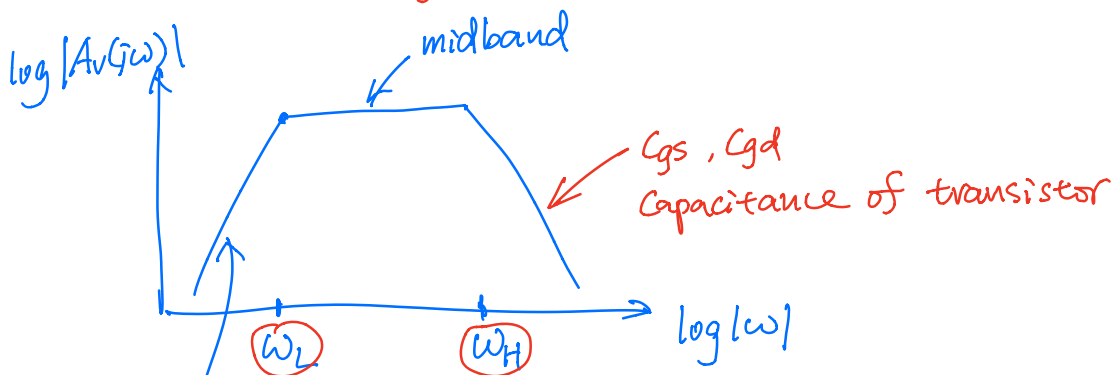
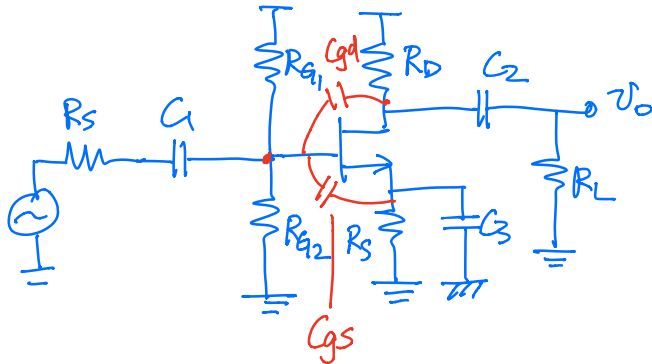


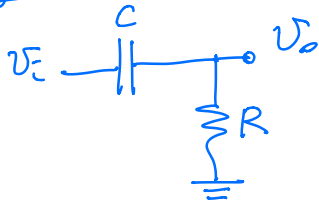
Frequency Response



Coupling and bypass

C_1, C_2 C_3
 $\sim 10\text{ nF}$

High-pass filter

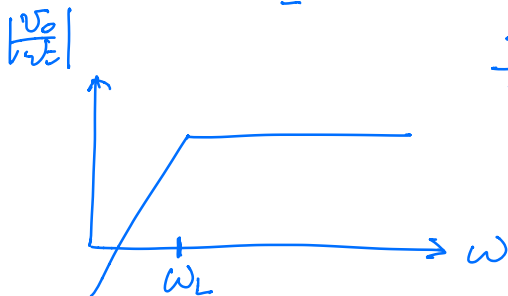


$$\frac{V_o}{V_i} = \frac{R}{\frac{1}{j\omega C} + R} = \frac{j\omega RC}{1 + j\omega RC} = \frac{j\omega}{\frac{1}{RC} + j\omega}$$

$$\omega_L = \frac{1}{RC}$$

$$s = j\omega$$

$$\frac{V_o}{V_i} = \frac{s}{s + \omega_L}$$



$$s \rightarrow 0 \quad \frac{V_o}{V_i} \rightarrow \frac{s}{\omega_L}$$

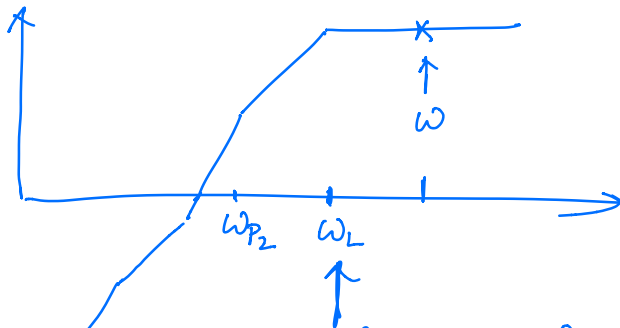
$$s = j\omega_L \quad \frac{V_o}{V_i} \rightarrow \left| \frac{1}{1+j} \right| = \frac{1}{\sqrt{2}}$$

$$s \rightarrow \infty \quad \frac{V_o}{V_i} \rightarrow 1$$

General Expression

$$F_L(s) = \frac{s^{n_L} + d_1 s^{n_L-1} \dots}{s^{n_L} + e_1 s^{n_L-1} \dots} = \frac{(s + \omega_{z1})(s + \omega_{z2}) \dots}{(s + \omega_{p1})(s + \omega_{p2}) \dots}$$

$n_L = \# \text{ of poles} = \# \text{ of zeros}$



Highest pole freq = ω_L : Low cut-off freq.

$$\begin{aligned} & (s + \omega_{p1})(s + \omega_{p2}) \dots (s + \omega_{pn_L}) \\ &= s^{n_L} + s^{n_L-1} (\omega_{p1} + \omega_{p2} + \dots + \omega_{pn_L}) + s^{n_L-2} (\omega_{p1}\omega_{p2} + \dots) \end{aligned}$$

$$s = |j\omega| \gg \omega_{pi}$$

$$\approx \left(s + \underbrace{\sum_{i=1}^{n_L} \omega_{pi}}_{\omega_L} \right) (s^{n_L-1} + \dots)$$

$$\omega_L = \sum_{i=1}^{n_L} \omega_{ci} = \sum_{i=1}^{n_L} \frac{1}{R_i C_i}$$

$C_i = \text{coupling or bypass caps.}$

Short-Circuit Time Constant (SCTC) Method:

1. Find all coupling and bypass caps.
2. Solve one cap at a time,
Replace all other capacitor with short circuits
3. Replace indep voltage source by short circuit,

" current " by open "

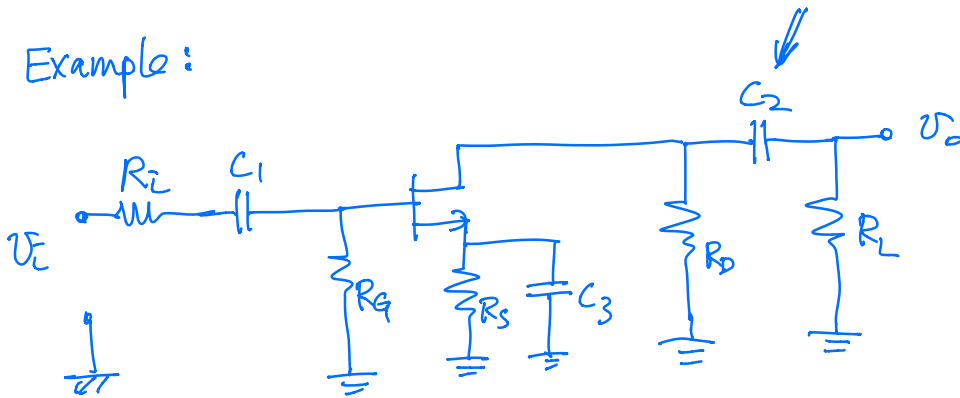
4. Calculate resistance R_i that's in parallel with C_1

5. Find time constant associate with C_1

$$\frac{1}{R_i C_1}$$

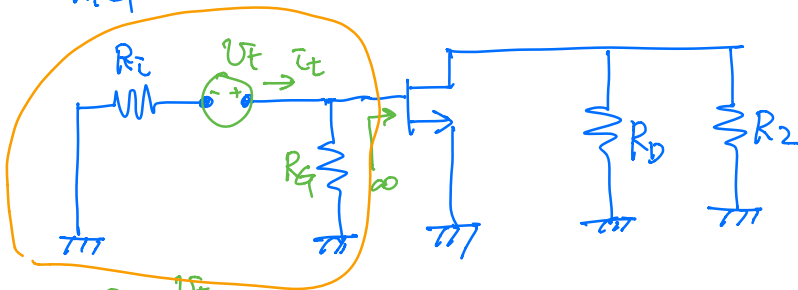
$$6. \omega_L = \sum_{i=1}^{n_L} \frac{1}{R_i C_i}$$

Example :



$$R_g = R_{g1} // R_{g2}$$

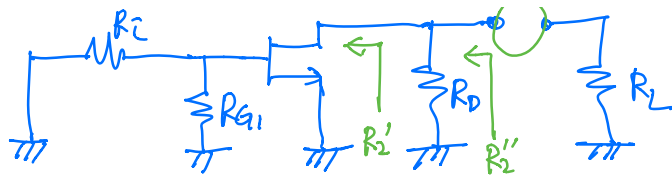
$$\tau_1 = \frac{1}{R_i C_1} \Rightarrow \text{Find } R_i$$



$$R_i = \frac{V_t}{i_t}$$

$$R_i = R_i + R_g$$

$$\tau_2 = \frac{1}{R_2 C_2}$$

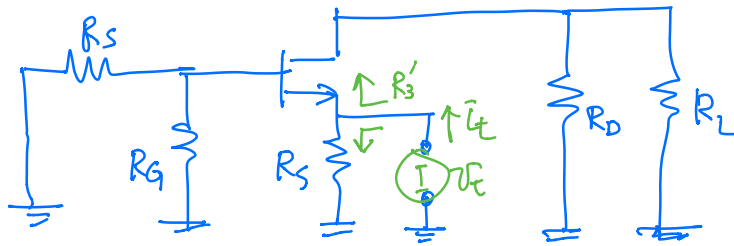


$$R_2' = r_o$$

$$R_2'' = r_o \parallel R_D \approx R_D \quad \because r_o \gg R_D$$

$$R_2 = R_2'' + R_L = R_D + R_L$$

$$\tau_3 = \frac{1}{R_3 C_3}$$



$$R_3' \approx \frac{1}{g_m}$$

$$R_3 = \left(\frac{1}{g_m}\right) \parallel R_S$$

$$\omega_L = \frac{1}{R_1 C_1} + \frac{1}{R_2 C_2} + \frac{1}{R_3 C_3} = \frac{1}{(R_i + R_s) C_1} + \frac{1}{(R_D + R_L) C_2} + \frac{1}{\left[\left(\frac{1}{g_m}\right) \parallel R_S\right] C_3}$$

↙ smallest time constant dominant
↓

Much simpler than solving small signal circuit with C_1, C_2, C_3

$\frac{1}{g_m}$ smallest resistance

As a designer, determine the minimum capacitance you need for C_1, C_2, C_3

$$\omega > \omega_L$$

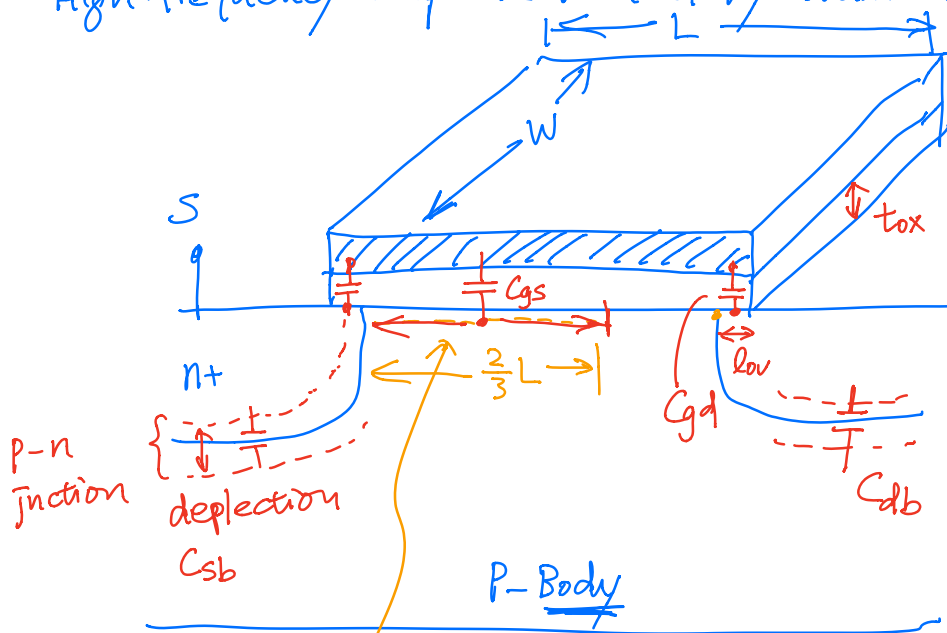
$$\left| \frac{1}{j\omega C_1} \right| < R_1 = R_i + R_s \Rightarrow C_1 \gg \frac{1}{\omega (R_i + R_s)} \quad \omega = \text{operating freq.}$$

$$\left| \frac{1}{j\omega C_2} \right| < R_2 = R_D + R_L \Rightarrow C_2 \gg \frac{1}{\omega(R_D + R_L)}$$

$$\left| \frac{1}{j\omega C_3} \right| < R_3 = \left(\frac{1}{g_m} \right) \parallel R_S \Rightarrow C_3 \gg \frac{1}{\omega \cdot \left(\frac{1}{g_m} \parallel R_S \right)}$$

↑

High frequency Response limited by transistor cap.



electron inversion layer covers partially channel

$$C_{gs} = C_{ox} \left(\frac{2}{3} L \right) \cdot W = \frac{2}{3} W \cdot L \cdot C_{ox} \quad ; \quad C_{ox} = \frac{\epsilon_{ox}}{t_{ox}}$$

↑
[F/cm²]

usually the largest cap of MOSFET

$$C_{gd} = \text{Overlap} = C_{ox} \cdot l_{ov} \cdot W \quad l_{ov} \ll L$$

$$C_{gd} \ll C_{gs}$$

$$C_{sb} = C_{jsb} \cdot \text{area}$$



$$\frac{C_{j0}}{\sqrt{1 + \frac{V_{bs}}{V_{bi}}}}$$

junction cap.

